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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Case Docket No. YKI-0034

The Assistant Commissioner of Patents
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JCS5 U.S. PTO
09/52/410
03/16/00

Sir:

Transmitted herewith for filing is the patent application of:

INVENTOR(S): BING LIN YANG

FOR: SURGE ABSORBER WITHOUT CHIPS

Enclosed are:

☒ 29 pages of specification.

☒ 7 claims.

☒ 8 sheet(s) of drawing(s).

☒ Declaration and Power of Attorney

☒ Information Disclosure Statement.

☐ An associate power of attorney.

☐ An assignment of the invention to _____.

☒ A certified copy of a Japanese application.

☒ A verified statement to establish small entity under 37 CFR 1.9 and 37 CFR 1.27.

The filing fee has been calculated as shown below:

			SMALL ENTITY		LARGE ENTITY	
FOR:	# FILED	# EXTRA	RATE	FEE	RATE	FEE
BASIC FEE	////////	////////	\$345	\$345.00	\$690	\$ 690
TOTAL CLAIMS (20)	12	0	X 9		x 18	
INDEP. CLAIMS (3)	2	0	x 39		x 78	
MULTIPLE DEPENDENT CLAIMS	0	0	+130		+260	
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[X] Any additional filing fees required under 37 CFR 1.16.

[X] Any patent application processing fees under 37 CFR 1.17.

Please file this application and conduct all future correspondence with Applicant's attorney identified below.

Respectfully Submitted,

BING LIN YANG

Applicant's Attorney

By: 

Edward J. Ellis
Registration No. 40,389
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Applicant: Bing Lin YangSerial or Patent No: _____ Attorney's Docket No: YKI-0034Filed or Issued: March 16, 2000For: Surge Absorber Without Chips

VERIFIED STATEMENT (DECLARATION) CLAIMING SMALL ENTITY
STATUS (37 CFR 1.9 (f) AND 1.27 (c)) - INDEPENDENT INVENTOR

As a below-named inventor, I hereby declare that I qualify as an independent inventor as defined in 37 CFR 1.9(c) for purposes of paying reduced fees under Section 41(a) and (b) of Title 35, United States Code, to the Patent and Trademark Office with regard to the invention entitled


SURGE ABSORBER WITHOUT CHIPS described in

☒ the specification filed herewith
☐ Application Serial No. _____, filed _____
☐ Patent No. _____, Issued _____

I have not assigned, granted, conveyed or licensed and am under no obligation under contract or law to assign, grant, convey or license, any rights in the invention to any person who could not be classified as an independent inventor under 37 CFR 1.9(c) if that person had made the invention, or to any concern which would not qualify as a small business concern under 37 CFR 1.9(d) or a nonprofit organization under 37 CFR 1.9(e).

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I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believe to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application, any patent issuing thereon, or any patent to which this verified statement is directed.

<u>Bing Lin YANG</u>		
NAME OF INVENTOR	NAME OF INVENTOR	NAME OF INVENTOR
		
Signature of Inventor	Signature of Inventor	Signature of Inventor
<u>Feb. 1, 2000</u>		
Date	Date	Date

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: BING LIN YANG

FOR: SURGE ABSORBER WITHOUT CHIPS

PRELIMINARY AMENDMENT

Box Patent Application
The Assistant Commissioner of
Patents and Trademarks
Washington, DC 02031

Sir:

Prior to the Examiner acting in the above-referenced application, please preliminary
amend the claims as follows:

IN THE CLAIMS:

Claim 3, line 1, please delete "or Claim 2".

Claim 5, line 1, please delete "or Claim 2".

Claim 6, line 1, please delete "or Claim 2".

Claim 7, line 1, please delete "or Claim 2".

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Please enter the following newly added claims:

8. (Newly Added) The surge absorber without chips defined in Claim 2,

wherein:

an air chamber provided in the housing is filled with clean, dry air, or a mixed gas
comprising clean, dry air and an insert gas or hydrogen gas.

9. (Newly Added) The surge absorber without chips defined in Claim 2;
wherein

said sealing spacers have a shape of a sphere or a cylinder with a central fitting through-
hole in which the lead portions of the lead terminals are inserted.

10. (Newly Added) The surge absorber without chips defined in Claim 2,
wherein

the lead terminals are formed of Dumet wires.

11. (Newly Added) The surge absorber without chips defined in Claim 2,
wherein

The lead terminals are formed of combined lead wires in which portions that weld with
the sealing spacers are composed of Dumet wires.

12. (Newly Added) The surge absorber without chips defined in Claim 8,
wherein:

the clean, dry air sealed in the air chamber has a relative humidity of 5% or less, and a
degree of cleanliness of 99.99% (0.5 μ mDOP), which is higher than the degree of cleanliness
obtained through filtering normal air.

REMARKS

Applicant requests entry of the above-identified amendments which, in part, reduce multiple dependencies and conform the claims to U.S. practice. No new matter is being introduced by this Amendment as antecedent support is set forth in the specification and the original claims.

Prosecution on the merits is respectfully requested.

If there are any charges with respect to this Amendment or otherwise, please charge them to Deposit Account No. 06-1130 maintained by Applicant's attorneys.

Respectfully submitted,

BING LIN YANG

CANTOR COLBURN LLP
Applicant's Attorney

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SURGE ABSORBER WITHOUT CHIPS

BACKGROUND OF THE INVENTION

5 1. Field of the Invention

The present invention relates to an electronic element, and in particular, to a surge absorber without chips.

2. Description of the Background Art

10 Stray waves, noise, and electrostatic disturbances which may cause surges are deeply-rooted obstacles to the most up-to-date electronic devices. In particular, high voltage pulse waves cause erroneous operations of semiconductor elements in electronic devices, and sometimes even damage the semiconductors or the devices themselves. Such problems can, however, be solved by the
15 use of surge absorbers.

A conventional surge absorber comprises a discharge chip or discharge core having an insulating microgap, and this discharge chip is sealed in a glass housing. For example, in a microgap surge absorber manufactured by Mitsubishi Materials K.K., a thin
20 conductive film is developed on a ceramic core, and cap-shaped metal electrodes are fixed on both ends of the core. Subsequently, the surface of the thin conductive film is removed by laser beam, forming a slit, or a microgap. A discharge chip (discharge core) formed in this way is then sealed in a glass tube. Accordingly,
25 in such a conventional chip type surge absorber, discharge voltage can be determined by the width of the microgap (the groove in a form of a thin slit).

Further conventionally known is a surge absorber constituted by conductive films partitioned by microgrooves. However, as it

is difficult to freely select a switching voltage in surge absorbers of this type, uses thereof are severely limited. To overcome this problem, U.S. Patent No. 4,727,350 discloses a surge absorber comprising a cylindrical tube core covered with a conductive film
5 having intersecting microgrooves, and sealed in a glass container.

In Japanese Patent Laid-Open Publication No. Hei 8-306467, the applicant of the present invention similarly proposed a surge absorber solving the conventionally existing problems described above. In this surge absorber, a tube core is arranged between
10 a pair of electrodes sealed in a housing, and an inert gas is filled in the surrounding air chamber around the core. This arrangement permitted surge absorption to a higher switching voltage than was conventionally possible.

However, each of the surge absorbers described above is
15 constituted by forming a discharge chip or discharge core (tube core) to first determine the discharge characteristic, and then sealing this chip or core in a housing. The structure is therefore complex and requires many manufacturing processes, making it difficult to reduce production costs. Especially in recent years,
20 many surge absorbers must be mounted in an electronic equipment to protect elements inside and to cope with fluctuations in power supply voltage. In this situation, the problem exists wherein the number of surge absorbers used leads directly to the increase in cost of the overall equipment.

Moreover, in the conventionally proposed surge absorbers,
25 a discharge current flows via a tube core. These surge absorbers are therefore unable to cope with a high switching voltage of, for example, 10,000 volts, and cannot completely absorb a surge of large energy at the time of surge absorption. This causes the problem

that, due to the residual voltage, a follow-current (a current, caused by a residual voltage, that flows into the electronic equipment to be protected) is generated in the circuitry. Further, in conventional devices, it is a problem that the switching voltage
5 varies depending on the specification of the tube core.

To solve the above problems, the applicant of the present invention filed Japanese Patent Application No. Hei 10-189486 providing a surge absorber which can easily be manufactured in large quantities due to its remarkably simple structure and which is
10 applicable to a wide range of surge voltage and maximum surge current.

According to this prior-filed invention, an improved surge absorber was provided that can perform surge absorption over a wide range of switching voltages. The improved surge absorber
15 instantaneously absorbs a large amount of energy because resistance at the time of surge absorption is extremely reduced. Any residual voltage is reliably eliminated that conventionally remained after surge absorption, and accordingly, generation of follow-current by residual voltage is prevented. Further, this surge absorber
20 can be finely adjusted for a variety of discharge voltage, discharge speed, and surge withstand amount (surge current) by arbitrarily designing each part of the surge absorber.

The surge absorber of the prior-filed invention is characterized in that a pair of discharge electrodes having lead
25 terminals are arranged facing one another at a predetermined distance, and, while maintaining the predetermined distance, a housing is melted at both ends and the ends are welded to the electrodes or the lead terminals.

In the prior-filed invention, the pair of discharge

electrodes are thus accurately held in an arrangement such that they face one another at a predetermined distance in the housing. While maintaining this arrangement, the housing is heated, and the electrodes or the lead terminals are sealed to the housing by welding. In this way, it is possible to arbitrarily select the distance between the two discharge electrodes, and precise adjustment of the distance is facilitated.

However, in the above-described background art, a variety of discharge electrodes having different diameters must be employed to provide surge absorbers for varying levels of surge voltages. Characteristics of a surge absorber, especially surge voltage and maximum surge current, do depend on a variety of factors such as the gap between the facing pair of discharge electrodes, the size of the air chamber, and the shape of the electrodes. However, the diameter of discharge electrodes is a strong factor determining the surge absorber characteristics. A variety of discharge electrodes having different diameters must therefore be provided to deal with varying levels of surge voltages.

As a typical surge absorber without chips, there is known, for example, a gas tube arrester manufactured by Ishizuka Electronics K.K. In this conventional device, electrodes are arranged facing one another and maintained a predetermined distance apart by an insulating material such as glass. However, in this gas tube arrester, the distance between the facing electrodes is determined by the length of the insulating tube, and the insulating tube and the electrodes are welded together. This structure requires preparation of a great variety of insulating tubes having different lengths in order to provide different types of facing electrode pairs, namely, facing electrodes separated by varying

distances. Using this structure, it is therefore practically impossible to obtain a surge absorber without chips that is applicable to a wide range of discharge voltages and maximum surge current. Further, as the insulating tube is made of glass and the distance between the electrodes is determined by the length of the insulating tube, the crucial distance between the electrodes fluctuates if the insulating tube and the electrodes are welded by heating. Under these circumstances, heat welding cannot be used, and fusing must be performed at the contacting surfaces of the insulating tube and the electrodes. During the fusing process, the air chamber becomes severely contaminated by flux or the like, resulting in extreme deterioration of the discharge characteristics.

The present invention was created in light of the above problems. The object of the present invention is to provide an improved surge absorber having a simple structure and capable of easily providing multiple types of surge absorbers to deal with a wide range of surge characteristics.

SUMMARY OF THE INVENTION

To accomplish the above object, the present invention offers a surge absorber comprising a pair of lead terminals having broadened tips forming discharge electrodes, sealing spacers fitted and fixed on lead portions of said lead terminals, and a housing, wherein the pair of lead terminals each having the sealing spacers fixed thereon are inserted from open ends on both sides of the housing, and the two sealing spacers are fixed airtightly on the housing while the discharge electrodes are held in position facing one another at a predetermined distance.

Further, to accomplish the above object, the present invention offers a surge absorber comprising a pair of lead terminals having broadened tips forming discharge electrodes, sealing spacers fitted and fixed on lead portions of the lead terminals. and a housing. The pair of lead terminals each having the sealing spacers fixed thereon are inserted from open ends on both sides of the housing, and the two sealing spacers are welded on an inside wall of the housing while the discharge electrodes are held in position facing one another at a predetermined distance.

According to the present invention wherein the discharge electrodes are formed by broadening the tips of the lead terminals, discharge electrodes having a variety of sizes can be obtained from lead terminals having identical diameters by changing the degree of the broadening. Moreover, as the discharge electrodes of the present invention do not need to be fixed on the housing, discharge electrodes of different diameters can be placed inside housings having common sizes. By using lead terminals and housings both having identical diameters, and by selecting a variety of sizes of broadened diameters for lead terminal tips, surge absorbers corresponding to different levels of surge voltages can be provided easily.

Further, according to the present invention, the housing and the sealing spacers are fixed, or alternatively, the housing is heated to fuse with the sealing spacers, while the relative positions of the pair of lead terminals having the discharge electrodes are precisely maintained. In this way, the present invention is advantageous in that precise relative positioning of various types of electrodes can be accomplished very easily.

In another aspect, the surge absorber of the present

invention is characterized in that an air chamber in the housing is filled with clean, dry air, or a mixed gas comprising clean, dry air and an inert gas or hydrogen gas.

According to this aspect of the present invention, a pair
5 of discharge electrodes are sealed inside the housing while being separated by a predetermined air gap, and the air chamber is filled with clean, dry air or a gas mixture comprising clean, dry air and an inert gas or hydrogen gas. With this extremely simple structure, the surge absorber can adequately deal with a surge at a high
10 switching voltage. Moreover, as gas resistance in the air chamber is notably low at the time of insulation discharge, the operational resistance can be extremely low when the dielectric breakdown of the gas is caused by a surge voltage. Therefore, a surge can be absorbed instantaneously even at a high switching voltage, thus
15 effectively preventing generation of typical residual voltage. Whereas a surge absorber having a gap of normal air disposed between electrodes is conventionally known as the above-mentioned gas tube arrester, the air sealed in the air chamber of the present invention is sufficiently clean and dry air. Dielectric breakdown therefore
20 occurs in the air chamber maintained in a stable condition between the facing electrodes, thus securely providing a very useful surge absorption path.

Further, according to the present invention, the clean, dry air sealed in the air chamber may have a relative humidity of 5%
25 or less, and a degree of cleanliness of 99.99% (0.5 μ mDOP), which is higher than the degree of cleanliness obtained through filtering normal air.

It is to be understood that, instead of the clean air, an inert gas or an active gas such as hydrogen can be sealed in the

air chamber of the present invention. Using air including 1 to 10% hydrogen, results were obtained wherein the discharge voltage tended to be lower and the response speed of discharge became faster. Further advantageously, the air in the air chamber is cleaned by the reduction effect of hydrogen gas, thus preventing deterioration of discharge characteristics even after a large number of discharge operations, and thereby enabling stable surge absorption.

Another feature of the present invention is that the sealing spacers may be in the shape of a glass or plastic sphere with a central fitting through-hole, and may also have the shape of a flat cylinder (a disk) instead of a sphere. These sealing spacers are heated while the lead portions of the lead terminals are inserted in the fitting through-holes, and the sealing spacers and the lead terminals are thus easily bonded by welding. When using disk-shaped sealing spacers, the spacers can be entirely reshaped into spheres when heated during welding mentioned above. Such spherical sealing spacers are advantageous in that they can very easily be inserted into the housing.

In consideration of the welding of the sealing spacers and the lead terminals, the lead terminals are preferably formed of Dumet wires. When the lead terminals are required to be long, it is also preferable to use a combined lead wire, in which only the lead portions to be welded to the sealing spacers are formed of Dumet wires, and other portions are formed of a typical, inexpensive material such as iron.

Moreover, in the surge absorber without chips of the present invention, at least one of the pair of discharge electrodes may have a flat discharge surface contacting the air gap.

The housing of the surge absorber without chips of the present

invention may comprise a glass ceramic or plastic container.

Air to be sealed in the air chamber according to the present invention is, as explained above, not typical air but clean and dry air. Its degree of cleanliness is 99.99% ($0.5\mu\text{MDOP}$), which
5 is higher than the degree of cleanliness obtained through filtering normal air. With regards to dryness, relative humidity is 5% or less, preferably 3% or less. Further, for example, to adjust the surge switching voltage, it is possible to mix inert gases in the air to be sealed in the air chamber as necessary. Argon or neon
10 are preferably used as an inert gas to be mixed. Nitrogen can also be used instead of these inert gases.

The above-described surge absorber without chips can be used widely in extremely complex electronic circuits that are important components for resetting computers operating at a high speed using
15 large-capacity memories. The use of the surge absorber can effectively eliminate influences of surge waves generated by frequent ON/OFF operations of computer displays and other electronic devices.

Moreover, the surge absorber without chips according to the
20 present invention can also be used in devices to be connected to telephone lines, such as a telephone set, a radio, a facsimile, a modem, and a program-controlled telephone switching device, devices to be connected to antennas or signal wires, such as an amplifier, a tape recorder, a vehicle radio, a radio transceiver,
25 and a sensor signal wire, devices requiring prevention of static electricity, such as a display and a monitor, domestic appliances, and computer controlled electronic devices. The surge absorber without chips of the present invention also functions as an over-voltage prevention device. In other words, the surge

absorber is an electronic element effective for counteracting hazardous influences of electric noise.

BRIEF DESCRIPTION OF THE DRAWINGS

5 The present invention will be further understood from the following description with reference to the accompanying drawings in which:

 Fig. 1 is a diagram illustrating the basic structure of a surge absorber of the present invention;

10 Fig. 2 is a diagram showing another preferred embodiment of the surge absorber of the present invention;

 Fig. 3 is a diagram showing a further preferred embodiment having a structure similar to that of Fig. 2;

 Fig. 4 is a diagram explaining the pressing process for
15 forming discharge electrodes at the tips of the lead terminals according to the present invention;

 Fig. 5 is a diagram for further explaining the pressing process illustrated in Fig. 4, showing a discharge electrode formed at the tip of a lead terminal;

20 Fig. 6 is a diagram illustrating that discharge electrodes having a variety of diameters can be inserted into a common housing according to the present invention;

 Figs. 7A and 7B show the assembly processes of a surge absorber of the present invention;

25 Figs. 8A and 8B show the assembly processes of a surge absorber of the present invention having another shape;

 Fig. 9 is a diagram illustrating another embodiment in which a protruding portion is disposed on one of the discharge electrodes;

 Fig. 10 is a diagram illustrating a conical protruding

portion on one of the discharge electrodes;

Fig. 11 is a diagram showing conical protruding portions disposed on the surfaces of both of the discharge electrodes;

Fig. 12 is a diagram of an embodiment having conical
5 concavities disposed on both of the discharge electrodes;

Fig. 13 is a diagram of an embodiment of the present invention having discharge electrodes of different shapes;

Fig. 14 is a diagram of an embodiment having arc-shaped surfaces on both of the discharge electrodes;

10 Fig. 15 is a diagram showing an example press die for machining the tips of lead terminals;

Fig. 16 is a diagram showing a discharge electrode formed by the press die shown in Fig. 15;

Fig. 17 is a diagram illustrating another preferred
15 embodiment of a lead terminal of the present invention;

Fig. 18 is a diagram showing the surge absorber of the present invention in a packaged state;

Fig. 19 is a graph showing a waveform of a surge applied to the present invention; and

20 Fig. 20 is a characteristic graph illustrating the state when the surge voltage of Fig. 19 is absorbed by the present invention.

DESCRIPTION OF PREFERRED EMBODIMENTS

Preferred embodiments of the present invention will now be
25 described referring to the accompanying drawings.

Fig. 1 shows a preferred embodiment of a surge absorber without chips of the present invention. A housing 10 comprises a glass or plastic cylinder, and an air chamber 12 is provided inside the housing. Lead terminals 14, 16 are inserted from openings on

both ends of the housing 10. In the air chamber 12, discharge electrodes 18,20 formed by broadening the diameter of the tips of the lead terminals 14,16 are arranged facing one another at a predetermined distance.

5 The feature of the present invention is that the discharge electrodes 18,20 are formed by broadening the diameter of respective tips of the lead terminals 14,16. The broadening process is described later in detail.

10 Protruding portions 18a,20a are formed in the center of each of the discharge electrodes 18,20. With this arrangement, when a surge voltage is applied to the lead terminals 14,16, a stable discharging easily occurs between the protruding portions 18,20a. Both of the discharge electrodes 18,20 are formed by broadening the leads into disk shapes. The diameters of the electrodes can
15 be selected as desired during the broadening process. Through the selection of desired diameters in addition to desired gaps between the discharge electrodes 18,20, surge absorbers corresponding to a variety of different surge voltages can be obtained. To precisely maintain the lead terminals 14,16 in position inside the
20 housing 10, the lead terminals 14,16 are provided with sealing spacers 22,24 fixed by welding. The lead terminals 14,16 having the sealing spacers 22,24 are inserted into the housing 10 from the openings on both ends of the housing 10. In that state, the sealing spacers 22,24 and the housing 10 are heated, and the spacers
25 and the housing become welded to each other as shown in the Figure.

 In a typical example, when fusing the sealing spacers 22,24 to the lead terminals 14,16, a burner can be used to perform welding at a heating temperature of, for example, 350 to 850°C. In contrast, when heat welding the sealing spacers 22,24 precisely held in

position in the housing 10, it is preferable to execute the heating to the similar temperature of 350 to 850°C in a slow manner.

To facilitate welding to the sealing spacers 22,24, the lead terminals 14,16 of the present invention are preferably formed of
5 Dumet wires, for example. A Dumet wire comprises a core wire material made of iron-nickel alloy, plated on its surface by a plating material mainly composed of copper and a nitrate. This copper component and the sealing spacers 22,24 made of glass are firmly welded to each other.

10 As is apparent from Fig. 1, the discharge electrodes 18,20 of the present invention need not be fixed directly to the housing 10. A large air chamber 12 can therefore be secured inside the housing 10, rendering the discharge characteristics stable. Another advantage is that housings 10 having a common size can be
15 used for discharge electrodes 18,20 having different diameters, as explained above.

The present invention employs the principle of electrical energy consumption and absorption through conversion of electrical energy into light energy, and effectively absorbs high voltage
20 stray waves and surge pulses. The reaction characteristic of this absorber inherently differs from the luminous effect of a LED (light-emitting diode) or a discharge tubes which gradually weakens from high luminance to extinction.

As described above, according to the embodiment of the surge
25 absorber without chips of the present invention shown in Fig. 1, the air chamber 12 is provided between the discharge electrodes 18,20 arranged facing one another inside the housing 10. When a surge voltage is applied between the discharge electrodes 18,20, dielectric breakdown occurs in the air chamber 12, and the surge

energy is thereby converted and absorbed.

As can be seen in Fig. 1, conventional discharge chips or discharge cores are not used in the present invention. As explained above, the surge absorber without chips of the present invention simply comprises a pair of discharge electrodes 18,20 arranged facing one another inside a housing 10. To put this simple structure into actual practice, the present invention is constituted by arranging the lead terminals 14,16 having the pair of discharge electrodes 18,20 inside the housing facing one another. In a typical procedure, one of the lead terminals 14 is inserted into a hole formed in an underlying jig, and, at the same time, the housing 10 is also placed in the hole.

In this inserted state, as the outer diameter of the sealing spacer 22 of the present invention is selected to be slightly smaller than the inner diameter of the housing 10, there is no difficulty in placing the sealing spacer 22 inside the housing 10. At this point, the first lead terminal 14 and the housing 10 stand upright in the underlying frame.

Subsequently, the other lead terminal 16 is inserted into a hole formed in an upper frame. The upper frame is then positioned contacting the lower frame. In this state, where the upper and the lower frames are adhered to each other, as the outer diameter of the other sealing spacer 24 is also slightly smaller than the inner diameter of the housing 10, the other discharge electrode 20 sustained in the upper frame falls perpendicularly to become positioned against the surface of the first electrode 18.

Subsequently, the other discharge electrode 20 placed by the upper frame is pulled upward by a predetermined distance and maintained in that position. Any appropriate mechanism can be used

for this pulling up and maintaining operation, but the distance of the pulling up must be precisely controlled according to the required accuracy. When preparation is completed in such a manner, the distance between the discharge electrodes 18,20 should be
5 accurately adjusted to a predetermined value.

The upper and the lower frames are then placed in a high temperature state. Alternatively, the above-described preparatory assembly procedure is performed at a high temperature from the beginning. The sealing spacers 22,24 and the housing 10,
10 together with the upper and the lower frames, are thus heated. When heated at 350 to 850°C, the sealing spacers 22,24 and the housing 10 typically melt and become firmly welded at both ends of the housing as shown in Fig. 1. According to the present invention, the upper and the lower lead terminals 14,16 are accurately held
15 in position in the jig during the heating at a high temperature and the subsequent cooling. As the welding to the housing 10 is carried out while the gap between the electrodes are maintained as such, it should be apparent that an accurate gap between the discharge electrodes can be provided as shown in Fig. 1 according
20 to the present invention.

Further according to the present invention, this gap can be controlled to any distance by appropriately adjusting the distance between the discharge electrodes sustained in the upper and the lower frames. Unlike the conventional gas tube arrester, a wide
25 variety of surge absorbers without chips can be obtained very easily and accurately.

Another feature of the present invention is that gas sealed in the air chamber 12 is clean, dry air, or a mixture of clean, dry air and an inert gas or hydrogen.

Preferably, the cleanliness of the air is 99.99% (0.5 μ mDOP), which is higher than the degree of cleanliness obtained through filtering normal air. With regards to dryness, relative humidity is preferably kept at 5% or less, more preferably at 3% or less.

5 In the present embodiment, normal air was filtered by an ATOMOS Ultra ULPA Filter manufactured by Nippon Muki K.K., and up to 99.9999% of particulates as small as 0.05 μ m therein were collected. Air obtained as such was accumulated and used.

By using such clean, dry air, the dielectric breakdown
10 voltage of the air chamber becomes extremely stable. More specifically, in the dielectric breakdown of the present invention, a spark of dielectric breakdown is generated on a part of the protruding portions 18a, 20a in the air chamber 12 shown in Fig. 1 when a surge voltage applied between the discharge electrodes
15 18, 20 exceeds a predetermined switching voltage. This dielectric breakdown instantaneously extends to the entire air chamber 12. As clean, dry air is uniformly subject to dielectric breakdown in an extremely short period of time, a large insulating current can flow between the discharge electrodes in the present invention.

20 As previously described, the surge absorber without chips of the present invention comprises the facing discharge electrodes 18, 20 arranged in the air chamber without any insulators disposed therebetween. It is therefore possible to eliminate occurrence of the unfavorable conventional phenomena of substantial decrease
25 in the distance between the discharge electrodes due to adhesion of copper molecules to the surface of the insulator generated at the time of discharge. A stable surge absorber with a long service life is thus provided by the present invention.

In the present invention, the switching voltage, the

insulating current (maximum surge current), and the operating speed are mainly determined by the volume of the air chamber 12, the gap length between the discharge electrodes 18,20, the diameter of the discharge electrodes, and the type and the pressure of the gas sealed. By altering any of these factors, surge absorbers having a variety of ranges of surge switching voltages can be obtained as desired. For the above-described embodiment illustrated in Fig. 1, switching voltages of approximately 50 to 15,000 volts or higher can be selected by appropriately choosing those factors.

The following table indicates typical examples of the distance between the discharge electrodes and the switching voltages according to the present invention.

Table 1:

Distance between discharge electrodes (mm)	DC discharge voltage (V)
0.04	120 - 400
0.08	200 - 500
0.12	300 - 700
0.15	400 - 800
0.2	500 - 1,000
1.0	700 - 1,200
1.5	1,000 - 1,500
2.0	1,200 - 2,000
2.5	1,800 - 3,200
3.2	2,500 - 6,000
3.9 - 4.3	4,000 - 12,000
4.5 - 4.8	8,000 - 15,000
5.2 -	15,000 -

A characteristic effect of the present invention is that, due to the clean, dry air, the allowable current density in the air chamber 12 at the time of dielectric breakdown can be notably increased. This means that the resistance of the clean, dry air

is low at the time of dielectric breakdown.

As dielectric breakdown occurs instantaneously and a large discharge current is allowed between the discharge electrodes 18,20, surge energy can be converted and absorbed instantaneously even
5 when a high surge voltage is applied. Accordingly, it is possible to reliably eliminate the conventional disadvantages such as the occurrence of residual voltage and the continued existence of follow-current due to the residual voltage.

As the glass cylinder forming the housing 10 of the present
10 invention, it is preferable to use, for example, a glass diode container of International Standard Type DO-34 with an inner diameter of 0.66mm. The lead terminals 14,16 are placed in the housing 10 from its both ends in a manner suitable for this inner diameter. The above-described sealing procedure is carried out
15 in a room with clean, dry air. As a result, clean, dry air is sealed inside the air chamber 12. Other plastic or shrink plastic materials can also be used for the housing 10.

Further, it is possible to use a container of International Standard Type DO-35 (inner diameter 0.76mm) or Type DO-41 (inner
20 diameter 1.53mm) as the housing 10. For a surge absorber with a large capacity, a glass diode container with an outer diameter of 9.0mm can be used. Moreover, the clean, dry air to be sealed in the air chamber 12 may be mixed with argon, neon, helium, and nitrogen. By appropriately selecting the ratio of the mixing,
25 surge voltage, maximum surge current, or reaction speed can be adjusted as desired.

According to the arrangement of the present invention wherein clean, dry air is sealed in the air chamber 12 as described above, when any surge voltage is selected from among 50 to 15,000 volts,

operating accuracy at each set voltage can be controlled within a range of approximately 10 volts by altering the surge characteristics by mixing the several gases mentioned above. The fact that such very fine adjustment is possible indicates that, as the sealed air is clean and dry, the distribution of molecules constituting the air inside the air chamber 12 is extremely uniform. Accordingly, the surge voltage once set by the volume of the air chamber 12 or the pressure or the type of the gas sealed is very stable.

Moreover, as the air is clean and dry, its resistance at the time of insulation is very low. The allowable surge current at the time of dielectric breakdown can therefore be large, permitting instantaneous absorption of surge energy even when a high surge voltage is applied. For example, using a surge absorber of the above-mentioned Japanese Patent Laid-Open Publication No. Hei 8-306467, if the surge voltage is set at 6,000 volts, a surge current of 1,050 amperes can be discharged by the surge absorber when a voltage of 10,500 volts is applied. However, even after the surge absorption, a residual voltage of 4,500 volts remains, resulting in generation of a follow-current of 450 amperes in the circuit. In contrast, under an identical condition, almost no residual voltage or follow-current results when using the surge absorber of the present invention.

Fig. 2 shows another embodiment of the surge absorber without chips of the present invention. This embodiment differs from the above-described embodiment shown in Fig. 1 in the manner of bonding of lead terminals 14,16 and the sealing spacers 122,124, and of the respective sealing spacers 122,124 and the housing 110.

In this embodiment, the sealing spacers 122,124 are composed

of a plastic material and fixed on each of the lead terminals 14,16 through formation using a mold. In the process of formation using a mold, as is known, the lead terminals 14,16 are fixed in spherical molds, and plastic material is injected therein. In this way, each
5 of the sealing spacers 122,124 is formed into one unit with a corresponding lead terminal.

The unit comprising the lead terminal 14 and the sealing spacer 122, and the other unit comprising the lead terminal 16 and the sealing spacer 124, are inserted from the open ends on both
10 sides of the housing 110, and accurately positioned by a predetermined jig such that a precise distance is maintained between the facing discharge electrodes 18,20. In this embodiment, the housing 110 is also made of plastic. In the accurately positioned state as described above, melted plastic is injected
15 in both of the open ends of the housing 110 as shown by numerals 50,52, thereby securely sealing the open ends in an airtight manner. It is to be understood that other appropriate adhesives may be used for the seals 50,52 in this embodiment.

Further, in this embodiment, the sealing spacers 122,124 and
20 the housing 110 may also be formed of other appropriate materials, such as ceramic materials.

Fig. 3 illustrates another alternative embodiment of the surge absorber without chips of the present invention. Although this embodiment has a structure similar to that shown in Fig. 2,
25 the sealing spacers 222,224 are in a shape of either a cylinder (disk) or a rectangular plate, and, matching this shape, the housing 110 is in a shape of a hollow cylinder or a hollow rectangular block. The materials which the components are made of, and the method of assembly, correspond to the embodiment of Fig. 2.

Figs. 4 and 5 illustrate the process of broadening the diameter of the lead terminal tips according to the present invention.

In Fig. 4, a Dumet wire cut into a predetermined length is used as the lead terminal 14. This lead terminal 14 is firmly held by clamps 30,32. The clamps 30,32 comprise claws that part in two for gripping onto the lead terminal 14. Disposed on each of the holding portions of the claws is a semicircular groove for fitting the lead terminal 14. The lead terminal 14 is held inside the semicircular grooves. Conical receiving surfaces 30a,32a are disposed on the upper surfaces of the clamps 30,32.

A press die 34 is located above the clamps 30,32. A conical concavity 34a is formed on the press die 34 at a position matching the axis of the lead terminal 14.

While in the state shown in Fig. 4, the press die 34 is pressed against the tip surface of the lead terminal 14. The diameter of the tip of the lead terminal 14 is thereby broadened as shown in Fig. 5. In this way, the discharge electrode 18 is formed on the tip of the lead terminal 14. During the above forming process, the outer diameter of the discharge electrode 18 can be selected as desired by adjusting the length of protrusion of the lead terminal 14 above the clamps 30,32 in the state shown in Fig. 4.

In this way, as previously mentioned, even when using lead terminals 14 having identical diameters, discharge electrodes 18 can be formed to have several different diameters 18A,18B,18C as shown in Fig. 6. These discharge electrodes with different diameters are easily inserted into housings 10 having identical diameters, thus providing surge absorbers that can deal with a wide variety of surge voltages.

Fig. 7A illustrates the process of welding a lead terminal 14 and a sealing spacer 22, and Fig. 7B illustrates the process of inserting the sealing spacer 22 into a housing 10.

In the present embodiment, a glass sphere is used as the sealing spacer 22. A fitting through-hole 22a is disposed in the center of the glass sphere 22. From the state shown in Fig. 7A, the lead terminal 14 is inserted into this fitting through-hole 22a. A discharge electrode 18 is already formed on the tip of the lead terminal 14 as shown in Figs. 4 and 5 explained above.

The lead terminal 14 and the sealing spacer 22 are welded together by heating. After those two components are formed into one unit as shown in Fig. 7B, the sealing spacer 22 is inserted from the opening of the housing 10 into the air chamber 12. By further heating at this point, the housing 10 and the sealing spacer 22 are welded and fixed as shown in Fig. 1.

According to the present invention, the sealing spacer 22 is not necessarily spherical. In Figs. 8A and 8B, a cylindrical (disk-shaped) sealing spacer 22 is used. As illustrated in Fig. 8A, a lead terminal 14 is inserted into the fitting through-hole 22a of the sealing spacer 22, and the sealing spacer 22 and the lead terminal 14 are welded together by heating as described above. In addition to being welded to the lead terminal 14 by heating, the sealing spacer 22 indicated by a dotted line in Fig. 8B changes its shape to a sphere indicated by a solid line when heated. The sealing spacer 22 can therefore be easily inserted into the housing 10 in a similar manner as in Fig. 7.

The cylindrical sealing spacer 22 shown in Fig. 8 is advantageous in that such a spacer can be obtained more easily at a lower cost compared with a spherical spacer.

Fig. 9 shows an embodiment similar to that shown in Fig. 1, and detailed descriptions will therefore be omitted here. The feature illustrated in Fig. 9 is that a protruding portion 18a is disposed on the surface of only one of the discharge electrodes 18. In the present embodiment, this one-sided protruding portion 18a induces dielectric breakdown in the air chamber 12 and allows stable setting of surge voltage level.

Fig. 10 shows a further different electrode shape. The entire surface of one discharge electrode 18 is in the shape of a cone, and its vertex forms the protruding portion 18a approaching closest to the other electrode 20. This embodiment is advantageous in that even when the tip of the protruding portion 18a is made to closely approach the other discharge electrode 20, the volume of the air chamber 12 is sufficiently large, permitting a large maximum surge current.

The embodiment shown in Fig. 11 has the structure in which the conical discharge electrode on one side in the above-described Fig. 10 is arranged on both sides. In this embodiment, as the conical vertices of the two discharge electrodes 18,20 are located close to one another, the surge switching voltage can be lowered. Another advantage of this embodiment is that as the volume of the air chamber 12 can be made large, the electrostatic capacity of the surge absorber can be reduced and the maximum surge current can be increased.

The embodiment shown in Fig. 12 comprises two discharge electrodes 18,20 each having a conical concavity. The resulting advantage is that the air chamber 12 becomes almost completely optically shielded from the outside. Specifically, in the embodiment of Fig. 12, such an electrode shape eliminates the

so-called "brightness effect" in which the surge voltage of a surge absorber varies due to incoming light. In general, when external light is strong, the surge voltage of a surge absorber tends to rise. However, in the present embodiment, entering of light into
5 the air chamber 12 from outside is reduced. Therefore, even when external light is strong, the conventional brightness effect can be minimized.

Fig. 13 shows a variation example of the embodiment of Fig. 12. According to this variation example, the two discharge
10 electrodes 18,20 are such that one has a convex surface and the other has a corresponding concave surface. By using such surface shapes for the discharge electrodes 18,20, the volume of the air chamber 12 can be arbitrarily selected, which determines the length of the gap inducing discharge and the maximum surge voltage.

Fig. 14 shows another surface shape for the discharge
15 electrodes 18,20. In this variation example, the surfaces of the discharge electrodes 18,20 are formed in the shape of an arc. As shown, in the present invention, the tips of the discharge electrodes need not be in the shape of pointed protruding portions.
20 Sufficiently successful discharge operation can similarly be performed using discharge electrodes having such gradually sloping arc-shaped tips. Further, in some cases, the pointed protruding portions on the tips of the discharge electrodes may essentially be formed into arc shapes, as shown in Fig. 14, by the effect of
25 multiple discharge operations. A surge absorber of the present invention effectively operated even after a discharging surface became arc-shaped due to repeated use.

In the present invention, the shapes of the lead terminal tips can be arbitrarily selected, as explained above. For instance,

the press die 34 of Fig. 4 may be shaped to have grid-like grooves formed on an end thereof, as shown in Fig. 15. Similar grid-like grooves 18b would then be formed on the surface of the discharge electrode 18 disposed on the tip of the lead terminal 14. As an
5 enlarged surface area can be employed for inducing discharge when a surge voltage is applied, this embodiment is useful when surge absorption should be performed from relatively low voltages.

In each of the above-described embodiments, the lead terminal 14 is composed of Dumet wires. However, as Dumet wires are
10 typically more expensive compared to lead wires made of a single material, using a long lead length would increase the cost of the surge absorber which would be disadvantageous. According to another embodiment of the present invention shown in Fig. 17, the lead terminal 14 comprises the tip portion 14a, the lead tail
15 portion 14b, and the welding portion 14c disposed between the two portions 14a, 14b. The tip portion 14a and the tail portion 14b are composed of inexpensive single-material wire, while only the welding portion 14c that welds with a sealing spacer is made of a combined lead wire such as Dumet wire. Connections between the
20 portions 14a and 14c and the portions 14b and 14c can be made easily by welding, and, as a whole, an inexpensive lead terminal is obtained.

Fig. 18 illustrates the surge absorber without chips of the present invention incorporated in a reinforcement package. In
25 cases when external force is applied to a surge absorber or if a surge absorber is used in an environment with vibrations, the housing may be damaged. In such cases, it is preferable to insert the entire surge absorber 100 in a ceramic package 60 and fix in position by molding with ceramic filler 62, as shown in Fig. 18.

The package 60 is box-shaped and is provided with grooves 60a,60b on a portion thereof. The lead terminals 14,16 of the surge absorber 100 are inserted in these grooves 60a,60b. In this state, the empty space inside the package 60 is filled with a ceramic material, and the entire unit is hardened. According to such an embodiment, a surge absorber can be obtained that has a sufficient mechanical strength for use in an electronic circuit located near a motor or other components. Packaging material is obviously not limited to ceramic. Materials such as plastic and metal can also be used.

Fig. 19 shows a waveform of a surge applied to a preferred embodiment of the present invention illustrated in Fig. 1. The surge voltage is 11,120 volts. Fig. 20 shows the state of the discharge voltage obtained when the surge voltage of Fig. 19 is applied to a 5,000-volt surge absorber of the present invention shown in Fig. 1. Surge absorption operation was activated at 5,280 volts. After approximately 70ns, the voltage is lowered to about 300 volts leaving almost no residual voltage and producing no follow-current.

According to the present invention, discharge electrodes facing one another inside a housing can be formed in a simple structure in the manners described above. The present invention provides a surge absorber having a long service life, excellent durability, and a broad range of applications in electrical devices.

What is claimed is:

1. A surge absorber without chips, comprising:
a pair of lead terminals having broadened tips forming
5 discharge electrodes;
sealing spacers fitted and fixed on lead portions of said
lead terminals; and
a housing; wherein
said pair of lead terminals each having said sealing spacers
10 fixed thereon are inserted from open ends on both sides of said
housing, and the two sealing spacers are fixed airtightly on said
housing while the discharge electrodes are held in position facing
one another at a predetermined distance.
- 15 2. A surge absorber without chips, comprising:
a pair of lead terminals having broadened tips forming
discharge electrodes;
sealing spacers fitted and fixed on lead portions of said
lead terminals; and
20 a housing; wherein
said pair of lead terminals each having said sealing spacers
fixed thereon are inserted from open ends on both sides of said
housing, and the two sealing spacers are welded on an inside wall
of said housing while the discharge electrodes are held in position
25 facing one another at a predetermined distance.
3. The surge absorber without chips defined in Claim 1 or Claim
2, wherein:
an air chamber provided in the housing is filled with clean,

dry air, or a mixed gas comprising clean, dry air and an inert gas or hydrogen gas.

4. The surge absorber without chips defined in Claim 3, wherein:

5 the clean, dry air sealed in the air chamber has a relative humidity of 5% or less, and a degree of cleanliness of 99.99% (0.5 μ mDOP), which is higher than the degree of cleanliness obtained through filtering normal air.

10 5. The surge absorber without chips defined in Claim 1 or Claim 2, wherein:

said sealing spacers have a shape of a sphere or a cylinder with a central fitting through-hole in which the lead portions of the lead terminals are inserted.

15 6. The surge absorber without chips defined in Claim 1 or Claim 2, wherein:

the lead terminals are formed of Dumet wires.

20 7. The surge absorber without chips defined in Claim 1 or Claim 2, wherein:

the lead terminals are formed of combined lead wires in which portions that weld with the sealing spacers are composed of Dumet wires.

ABSTRACT OF THE DISCLOSURE

There is provided a surge absorber without chips that has a simple structure, has a broad range of surge switching voltage, and can perform stable surge absorption. Discharge electrodes 18,20 are formed on tips of lead terminals 14,16 by pressing. Sealing spacers 22,24 are welded on the lead terminals 14,16. These spacers 22,24 are inserted inside a housing 10 and fixed on the housing 10 by welding. Discharge electrodes 18,20 having different diameters can be arbitrarily formed on lead terminals having identical diameters.

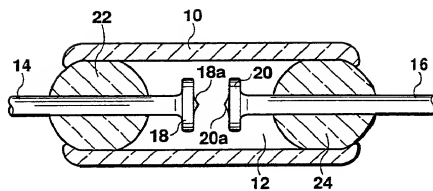


Fig. 1

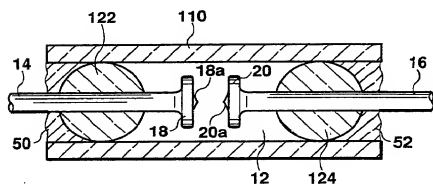


Fig. 2

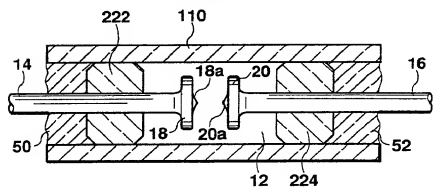


Fig. 3

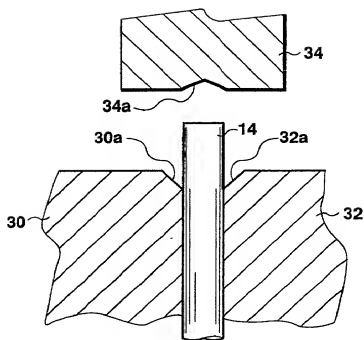


Fig. 4

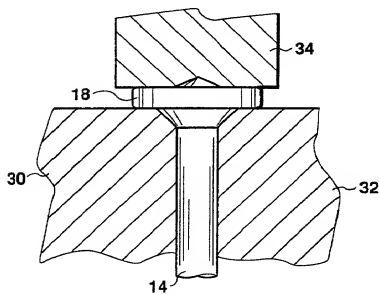


Fig. 5

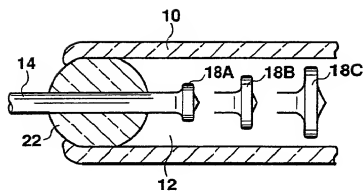


Fig. 6

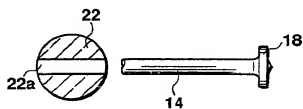


Fig. 7A

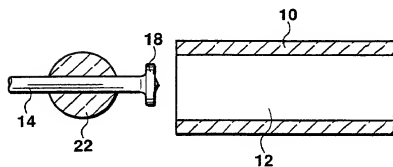


Fig. 7B

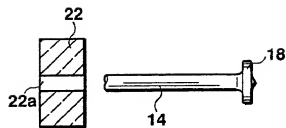


Fig. 8A

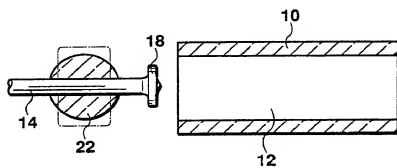


Fig. 8B

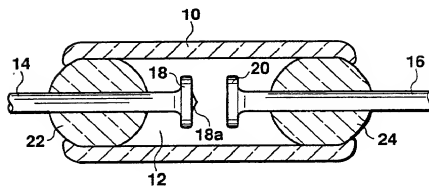


Fig. 9

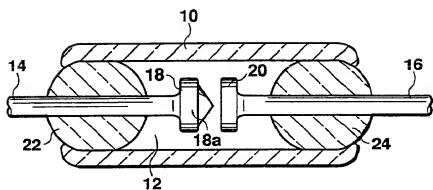


Fig. 10

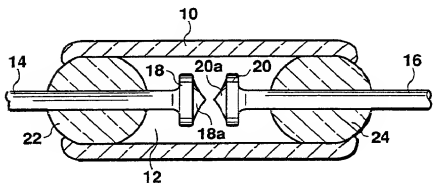


Fig. 11

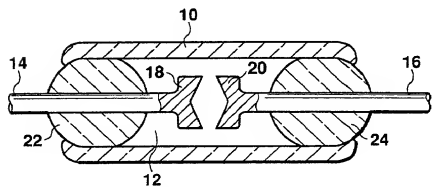


Fig. 12

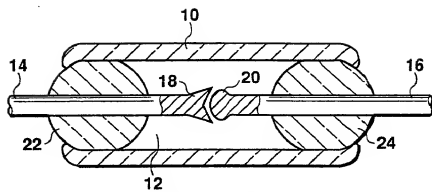


Fig. 13

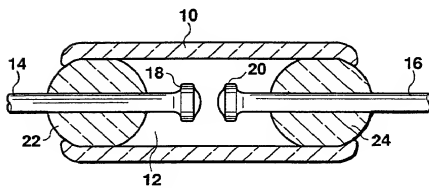


Fig. 14

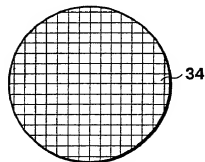


Fig. 15

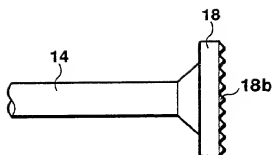


Fig. 16

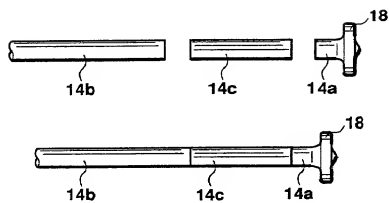


Fig. 17

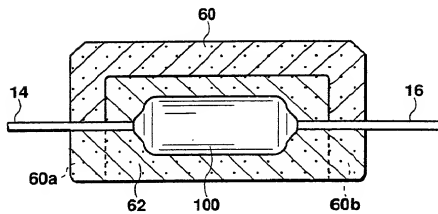


Fig. 18

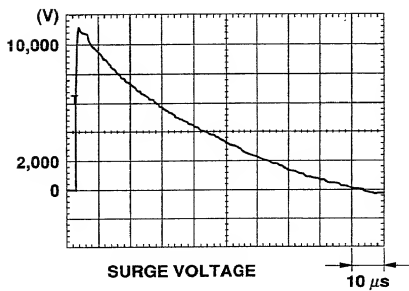


Fig. 19

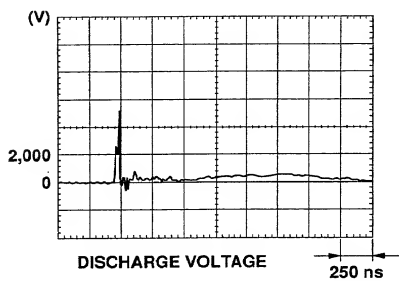


Fig. 20

Declaration and Power of Attorney For Patent Application

特許出願宣言書及び委任状

Japanese Language Declaration

日本語宣言書

下記の氏名の発明者として、私は以下の通り宣言します。

As a below named inventor, I hereby declare that:

私の住所、私書箱、国籍は下記の私の氏名の後に記載された通りです。

My residence, post office address and citizenship are as stated next to my name.

下記の名称の発明に関して請求範囲に記載され、特許出願している発明内容について、私が最初かつ唯一の発明者（下記の氏名が一つの場合）もしくは最初かつ共同発明者であると（下記の名称が複数の場合）信じています。

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled

SURGE ABSORBER WITHOUT CHIPS

上記発明の明細書（下記の欄でx印がついていない場合は、本表に添付）は、

the specification of which is attached hereto unless the following box is checked:

☐ 月 日に提出され、米国出願番号または特許協定条約国際出願番号を _____ とし、
（該当する場合） _____ に訂正されました。

☐ was filed on _____
as United States Application Number or
PCT International Application Number
_____ and was amended on
_____ (if applicable).

私は、特許請求範囲を含む上記訂正後の明細書を検討し、内容を理解していることをここに表明します。

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

私は、連邦規則法典第37編第1条56項に定義されており、特許出願の右欄について重要な情報を開示する義務があることを認めます。

I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, Section 1.56.

Japanese Language Declaration
(日本語宣言書)

私は、米国法典第35編119条(a)-(d)項又は365条(b)項に基づき下記の、米国以外の国の少なくとも一つを指定している特許協力条約365(a)項に基づく国際出願、又は外国での特許出願もしくは発明特許の出願についての外国優先権をここに主張するとともに、優先権を主張している、本出願の前に出願された特許または発明特許の外国出願を以下に、格点をマークすることで、示しています。

I hereby claim foreign priority under Title 35, United States Code, Section 119(a)-(d) or 365(b) of any foreign application(s) for patent or inventor's certificate, or 365(a) of any PCT International application which designated at least one country other than the United States, listed below and have also identified below, by checking the box, any foreign application for patent or inventor's certificate, or PCT International application having a filing date before that of the application on which priority is claimed.

Prior Foreign Application(s)

Priority Not Claimed
優先権主張なし

外国での先行出願
Hei 11-69662

Japan

16/03/1999

(Number)
(番号)

(Country)
(国名)

(Day/Month/Year Filed)
(出願年月日)

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(出願年月日)

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(Country)
(国名)

(Day/Month/Year Filed)
(出願年月日)

☐

私は、第35編米国法典119条(e)項に基づいて下記の米国特許出願規定に記載された権利をここに主張いたします。

I hereby claim the benefit under Title 35, United States Code, Section 119(e) of any United States provisional application(s) listed below.

(Application No.)
(出願番号)

(Filing Date)
(出願日)

(Application No.)
(出願番号)

(Filing Date)
(出願日)

私は、下記の米国法典第35編120条に基づいて下記の米国特許出願に記載された権利、又は米国を指定している特許協力条約365条(c)項に基づき権利をここに主張します。また、本出願の各請求範囲の内容が米国法典第35編112条第1項又は特許協力条約で規定された方法で先行する米国特許出願に開示されていない限り、その先行米国特許提出日以降で本出願書の日本国内または特許協力条約国際提出日までの期間中に入手された、連邦規則法典第37編1条56項で定義された特許資格の有無に関する重要な情報について開示義務があることを認識しています。

I hereby claim the benefit under Title 35, United States Code, Section 120 of any United States application(s), or 365(c) of any PCT International application designating the United States, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT International application in the manner provided by the first paragraph of Title 35, United States Code Section 112, I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, Section 1.56 which became available between the filing date of the prior application and the national or PCT International filing date of application.

(Application No.)
(出願番号)

(Filing Date)
(出願日)

(Status: Patented, Pending, Abandoned)
(現況: 特許許可済、係属中、放棄済)

(Application No.)
(出願番号)

(Filing Date)
(出願日)

(Status: Patented, Pending, Abandoned)
(現況: 特許許可済、係属中、放棄済)

私は、私自身の知識に基づいて本宣言書中で私が行なう表明が真実であり、かつ私の入手した情報と私の信じていること、さらに故意になされた虚偽の表明及びそれと同等の行為は米国法典第18編第1001条に基づき、罰金または拘禁、もしくはその両方により処罰されること、そしてそのような故意による虚偽の声明を行なえば、出願した、又は出願許可された特許の有効性が失われることを認識し、よってここに上記のごとく宣誓を致します。

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Japanese Language Declaration
(日本語宣言書)

委任状： 私は下記の発明者として、本出願に関する一切の
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